

FIG. 1

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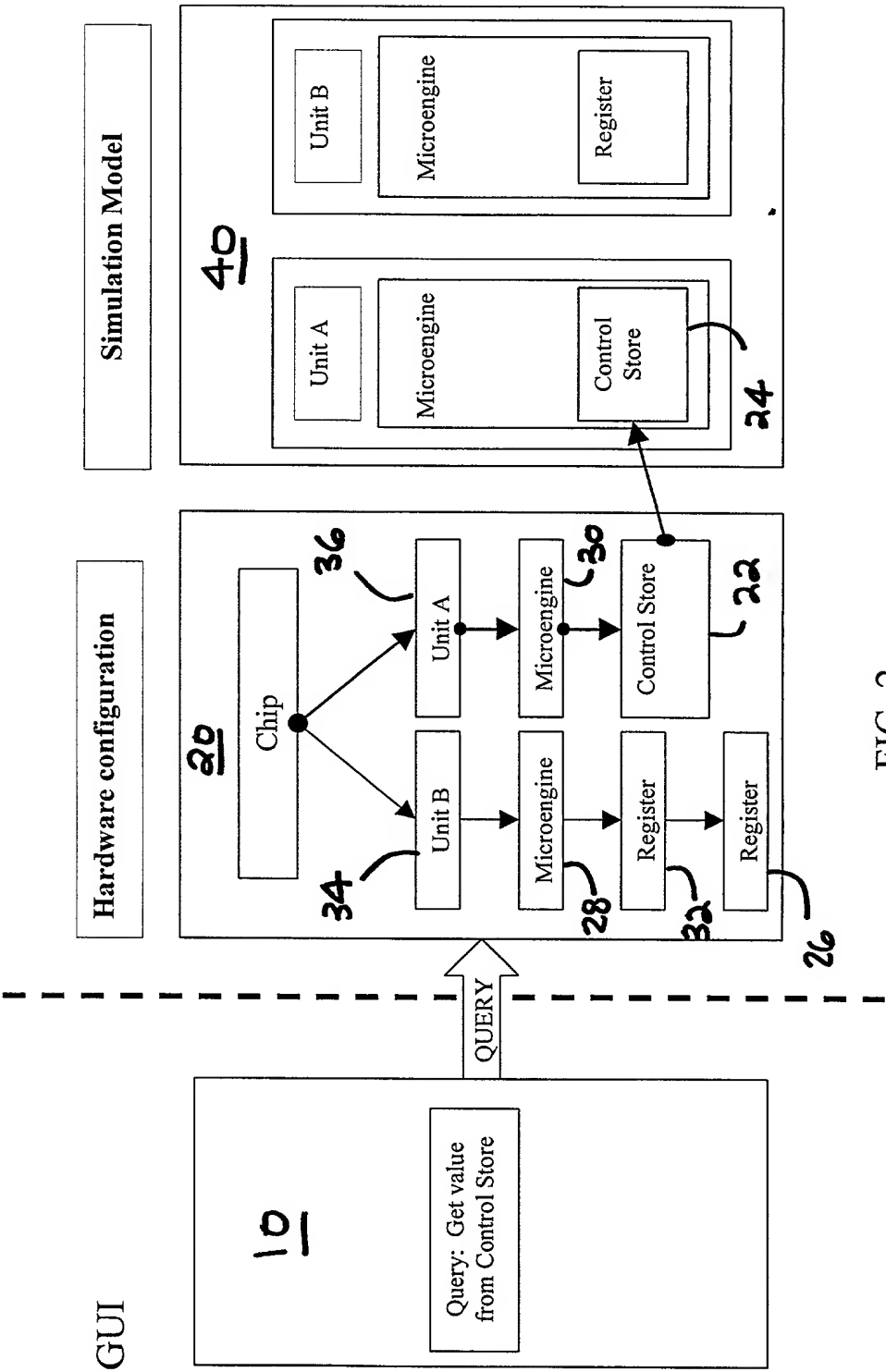


FIG. 2

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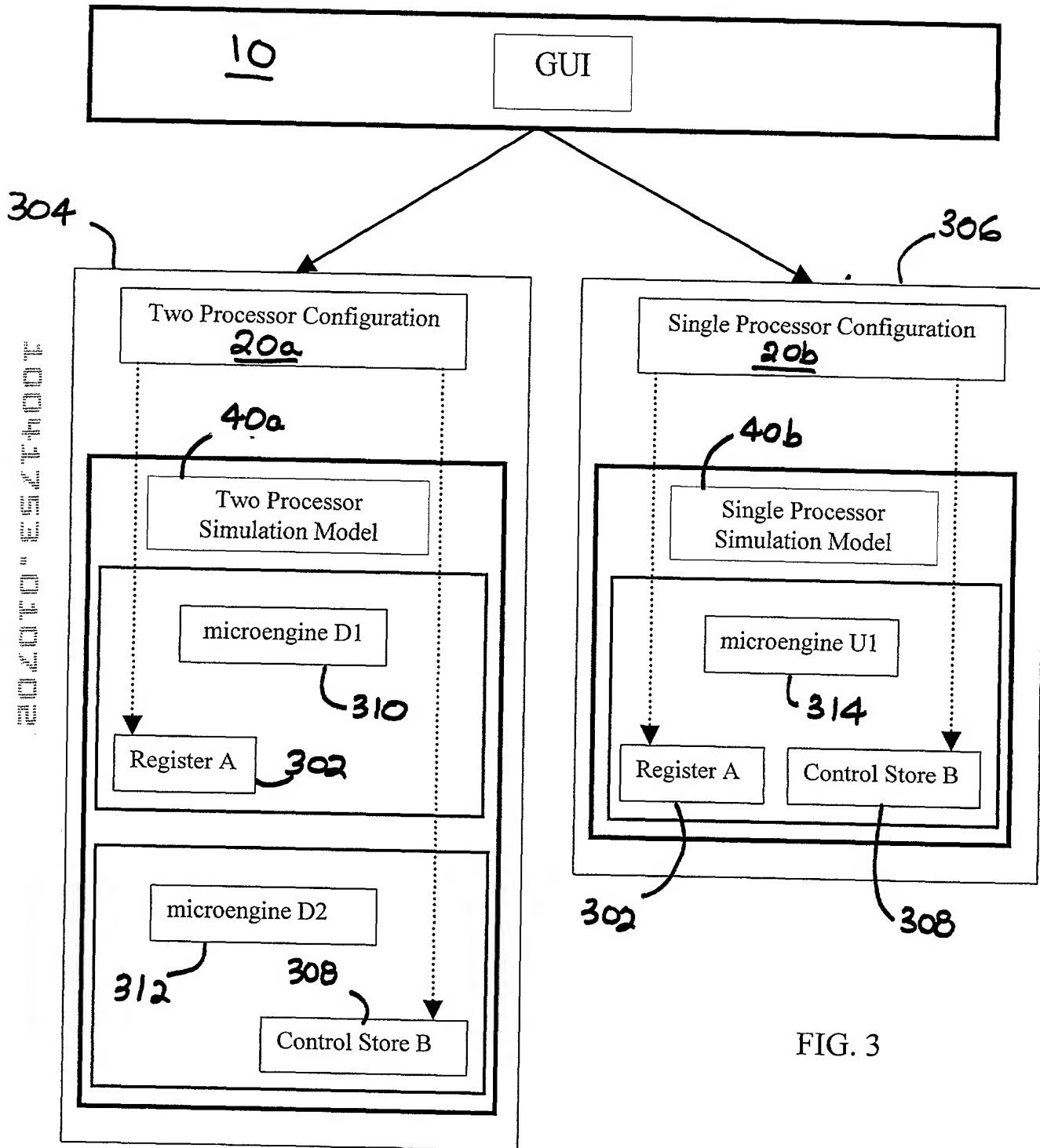


FIG. 3


```
DEF_CLOCK( K_CORE_CLK, clk );  
DEF_USTORE( 312.ram_ustore, ustore );  
DEF_ARRAY( 312.TE_GPR_AREG_FILE, gpr_a_regs );
```

**Figure 4A**

```
DEF_CLOCK( K_CORE_CLK, clk );  
DEF_USTORE( 314.ram_ustore, ustore );  
DEF_ARRAY( 314.TE_GPR_AREG_FILE, gpr_a_regs );
```

**Figure 4B**

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```
S_hw_config_chip *chip = new S_hw_config_chip( m_sim, this );  
m_sim->m_hw_config_root->link_chip( chip );  
  
S_hw_config_hw_unit *group = new S_hw_config_hw_unit();  
chip->link_subunit( group );  
  
S_hw_config_useq *useq = new S_hw_config_useq_fbox();  
group->link_subunit( useq );  
  
useq->associate_ustore( ustore, S_CONFIG_ARRAY__ME_USTORE );  
useq->associate_array( gpr_a_regs, S_CONFIG_ARRAY__GPR_A );
```

**Figure 5**

S\_hw\_config\_chip \*chip;  
S\_hw\_config\_hw\_unit \*subunit, \*subunit\_lower;  
S\_hw\_config\_sim \*root;  
S\_hw\_config\_array \*array;  
int matches;

root = S\_sim::getsim()->m\_hw\_config\_root;  
chip = root->m\_chip\_table[0];  
subunit = chip->m\_subunit\_table[0];

root->find\_unique\_array( S\_CONFIG\_ARRAY\_\_ME\_USTORE,  
&matches );  
root->find\_unique\_array( S\_CONFIG\_ARRAY\_\_GPR\_A,  
&matches );

**Figure 6**